

What is claimed is:

1. A power line communications transceiver comprising:
a plurality of hardwired logic (HWL) modules coupled to a plurality of digital signal processors (DSPs) in an architecture permitting processing of data and data signals associated with power line communications to be performed at least one of the DSPs and HWL modules, and
a memory including software code for execution by the DSP, wherein the DSP, based on execution of the software code, controls data signal exchange between or among the HWL modules and DSPs and processing of data signals received at and to be transmitted from the transceiver by at least one of the DSPs and the HWL modules.
2. The power line communication transceiver as claimed in claim 1, wherein the transceiver is implemented in PLC technology.
3. The power line communication transceiver as claimed in claim 1, wherein the architecture utilizes a plurality of buses for control information and data payload.
4. The power line communication transceiver as claimed in claim 3, wherein one of the plurality of buses is a control bus.
5. The power line communication transceiver as claimed in claim 4, wherein the architecture includes one or more functional blocks for reconfiguration through the control bus to a required function.
6. The power line communication transceiver as claimed in claim 3, wherein the plurality of buses is a dual bus for control and data.
7. The power line communication transceiver as claimed in claim 6, wherein the architecture is multi-based.